

FIG. 1

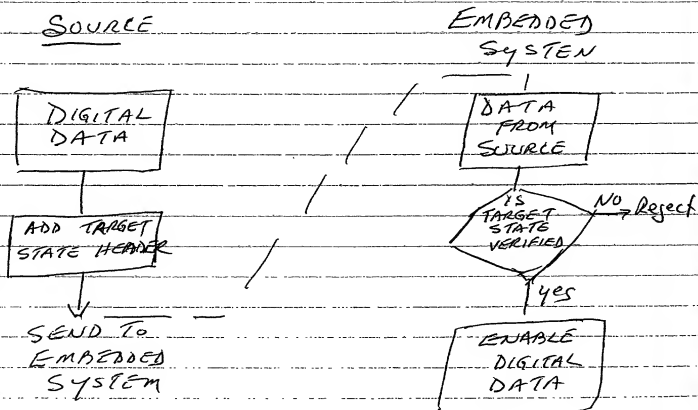
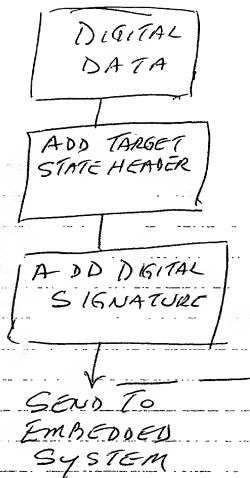


FIG. 2

SOURCE



EMBEDDED SYSTEM

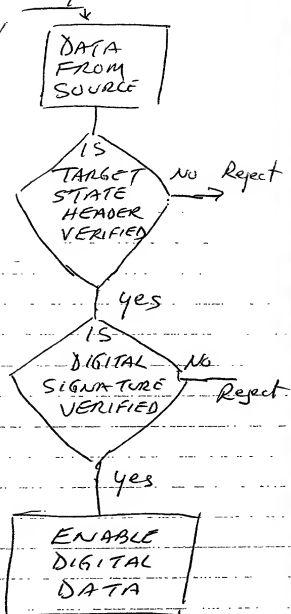


FIG 3

T	D	DIGITAL DATA
S	S	

FIG 4